

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

ASYNCHRONOUS SYSTEM-ON-A-CHIP INTERCONNECT

Application Number : 10/634597
Confirmation Number: 6534
First Named Applicant: URI CUMMINGS
Attorney Docket Number: FULCP009
Art Unit: 2631
Examiner: Mr. KHANH C. TRAN
Search string: (6002861 or 4773066).pn



Certification: This Information Disclosure Statement was submitted under the following conditions, which satisfies the requirement under 37 CFR 1.97(e). The filer certified:

That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6002861	1999-12-14	Butts et al.		395	500.37
	2	4773066	1988-09-20	Kirkman		370	85

Signature

Examiner Name	Date